Stub Loaded Rectangular Ring Shaped Tri-Band Monopole Antenna for Wireless Applications

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Abstract. This paper presents, design and equivalent circuit analysis of tri-band parasitically stub loaded rectangular ring printed monopole antenna for wireless communication applications. The printed monopole antenna is designed, fabricated and tested. The measured impedance bandwidth (S11 < -10 dB) is 750 MHz (2.22 – 2.97 GHz), 220 MHz (3.65 – 3.87 GHz) and 1.28 GHz (5.23 – 6.51 GHz) covering Wi-Fi, Wi-MAX and WLAN/Wi-MAX applications respectively. The electrical dimension of monopole is 0.245λd × 0.212λd. The proposed antenna is simple and a low profile structure fabricated using low cost glass epoxy substrate (FR4). The overall size of antenna is 60 × 40 mm². Using slot cut and parasitically coupled stub loading methods, the tri-band monopole antenna has been realized and measured results are found to be in good agreement with simulated results. The equivalent circuit analysis and mutual coupling between the slots are calculated and presented.

Keywords: Equivalent circuit, Rectangular Ring, Tri-band, WLAN/Wi-MAX

1. Introduction

The speedy growth in modern wireless communication systems demands compact and multiband antennas to cover wireless applications such as wireless local area network (WLAN: 2.4-2.48 GHz, 5.15-5.35 GHz, 5.725-5.825GHz), worldwide interoperability for microwave access (Wi-MAX: 2.5-2.69 GHz, 3.4-3.69GHz, 5.25-5.85GHz) etc. These communication systems need multiband antennas with wide bandwidth to cover the complete and desired bands of application. To meet the crucial requirements of wideband/multiband wireless communication systems, microstrip patch antennas (MPAs) are found suitable. However, a single band and narrow bandwidth is the main limitation of the microstrip patch antenna. These limitations can be overcome by using suitable techniques such as cutting the slot inside the radiating patch or loading stub to yield compact antenna or to achieve a multiband response, respectively, and use of a thicker substrate with low relative permittivity for bandwidth enhancement. The use of a thicker substrate can enhance the operating bandwidth but at the cost of higher volumetric size [1-2]. The limitation of size in a compact and modern wireless system is ubiquitous. Through using microstrip line feed and partial ground technique, a substrate with lower thickness can be used to realize compact and wideband antennas. Therefore, many antenna researchers are motivated to design compact, multiband antennas with a wideband response for various wireless communication applications [3-20].

2. Antenna geometry design

The geometrical design process of the proposed monopole antenna is depicted in Figure 1. Figure 2 presents the complete geometrical design of the proposed tri-band monopole antenna. The rectangular ring monopole antenna comprises antenna with rectangular slit cut [6], a paper-based inject printed U-slotted tri-band antenna [7], inverted L-shaped strip based bent fork-shaped compact antenna [8], a microstrip planar antenna with G-shaped slot and U-shaped stub for WLAN/WiMAX applications [9], CPW fed dual modified U slot-loaded monopole antenna for tri-band [10], U-shaped strip and L-shaped slot loaded monopole antenna [11] have been reported. The mutual coupling and coupling factor between the slots of microstrip antenna is reported [1,12]. The use of stubs or parasitic elements has used to design multiband antennas [18-19]. Some of the reported antenna is compact in size with complex design and comprises low fractional bandwidth in lower WLAN.

In this paper, a microstrip line fed rectangular ring monopole antenna loaded with a slit is proposed for tri-band WLAN/Wi-MAX operations. The slit is embedded into the rectangular ring patch to realize dual-band operations. Further, to achieve tri-band operation, parasitically coupled stub of quarter wavelength is placed on the partial ground. The proposed antenna is theoretically analyzed using the transmission line equivalent circuit and various lumped parameters are calculated. The mutual coupling and coupling factor between rectangular ring and slit of this antenna is calculated. The proposed antenna is fabricated using FR4 substrate having substrate parameters h = 1.6 mm, εr = 4.3, and tan δ = 0.02. The antenna is designed and simulated using the method of moments based CAD Feko [13]. The designed antenna structure is simulated using an infinite ground plane.
rectangular monopole having length and width of \( L \times W \). The length \( L \) of the rectangular patch is calculated using equation (1) and (2) [1-2].

\[
L = \frac{c}{4f_r \sqrt{\varepsilon_{re}}} \tag{1}
\]

\[
\varepsilon_{re} \approx \left( \frac{\varepsilon_r + 1}{2} \right) \tag{2}
\]

The rectangular ring is realized by cutting the rectangular slot inside the patch of size 18 \( \times \) 13 mm\(^2\). The microstrip feed line having width of \( w_f \) is used to feed the antenna. The partial ground of size \( L_g \times W_g \) is placed beneath the radiating patch. Further, a slit of dimension \( l_s \times w_s \) is incorporated into the rectangular ring to realize dual-band response as shown in Figure 1(b) (Antenna II). The location and dimension of the slit is optimized to get the desired dual-band response. To realize the tri-band response, a stub of approximately quarter wavelength at higher frequency is placed in the ground plane as shown in Figure 1(c) (Antenna III).

The complete geometrical design of tri-band printed monopole antenna is shown in Figure 2. The geometrical parameters with dimensions are given in Table 1.

\begin{table}[h]
\centering
\begin{tabular}{|c|c|c|}
\hline
Parameter & Dimension (mm) & Parameter & Dimension (mm) \\
\hline
\( L \) & 30 & \( l_s \) & 12 \\
\( W \) & 26 & \( w_s \) & 1.5 \\
\( L_g \) & 19 & \( L_s \) & 8 \\
\( W_g \) & 40 & \( W_r \) & 1.5 \\
g & 1 & \( w_f \) & 3 \\
\hline
\end{tabular}
\caption{Antenna geometrical parameters}
\end{table}

3. Equivalent circuit analysis

From Figure 2, it is seen that the tri-band antenna comprises three main sections as (i) the microstrip line fed rectangular ring microstrip patch antenna (RRMPA), (ii) slit embedded into rectangular ring antenna, and (iii) stub loaded antenna. The use of simple circuits based on lumped elements helps to gain an understanding of antenna design and analysis [20]. The analysis of the antenna is carried out using a transmission line equivalent circuit analysis. The microstrip patch antenna with a rectangular ring is fed using microstrip line. The microstrip line can be represented using two series inductors \( L_{ML} \), with shunt capacitor \( C_{ML} \) and rectangular ring microstrip patch antenna can be modelled using parallel \( R_l, L_l, C_l \) circuit [1] as depicted in Figure 3. As the current density is maximum at the edge of microstrip patch, a slit embedded into rectangular patch does not alter the performance characteristics of microstrip patch antenna.

The parallel \( R_l, L_l \) and \( C_l \) can be expressed as [1],

\[
C_1 = \frac{\varepsilon_{re} L W}{2h} \cos^2 \left( \frac{\pi y_0}{L} \right) \tag{3}
\]

\[
L_1 = \frac{1}{C_1 \omega_0^2} \tag{4}
\]

\[
R_1 = \frac{Q}{\omega_0 C_1} \tag{5}
\]

where, \( Q \) is quality factor and expressed as [14],

\[
Q = \frac{\sqrt{\varepsilon_{re}}}{4f_r h} \tag{6}
\]

In equations (3), (4), (5) and (6), \( c \) is velocity of light in free space and given by \( 3 \times 10^8 \) m/s, \( f_r \) is resonance frequency, \( h \) is substrate thickness, \( \omega_0 \) is angular frequency, \( L \) and \( W \) are length and width of rectangular microstrip antenna and \( \varepsilon_{re} \) is effective dielectric constant and expressed as [1, 14].

\[
\varepsilon_{re} = \left( \frac{\varepsilon_r + 1}{2} \right) + \frac{(\varepsilon_r - 1)}{2 \sqrt{1 + \frac{1}{\varepsilon_r}}} \tag{7}
\]
The calculated values of $L_1$ and $C_1$ are 9.28 $pF$ and 0.45 $nH$, respectively. The microstrip feed line is represented as series inductance and shunt capacitance and can be expressed as [1]

$$C_{ML} = \frac{w_f(9.5\varepsilon_r+1.25)w_f}{h} + 5.2\varepsilon_r + 7.0 \quad (8)$$

$$L_{ML} = 100.\ h (4 \sqrt{w_f} - 4.21) \quad (9)$$

In equation (8) and (9), $w_f$ is width of microstrip feed line and calculated as given in [15]. In further analysis, a slit of length $l_s$ and width $w_s$ is embedded into the RRMPA. This slit introduces a new band at around 3.5 GHz. The slit is perpendicular to the direction of vertical current and can be modeled as radiation resistance with reactance, as presented in Figure 4. The slit offers capacitive as well as inductive reactance $X_c$ and $X_l$, respectively. The microstrip feed line is represented as a parasitically loaded quarter wavelength stub.

The inductance $L_{st}$ and capacitance $C_{st}$ for resonator can be expressed by [17].

$$C_{st} = \frac{2w_f\varepsilon_r}{\eta_0} \quad (12)$$

$$L_{st} = \frac{1}{C_{st}\eta_0} \quad (13)$$

where, $L_s$ and $W_s$ are length and width of stub, respectively. The calculated values for inductance and capacitance are 0.99 $mH$ and $7.58 \times 10^{-19} F$, respectively. This resonator is parasitically coupled with main RRMPA; therefore, a fringing capacitance exists between RRMPA and stub. This fringing capacitance is expressed as [1].

$$2C_f = \varepsilon_0\varepsilon_r C_p \quad (14)$$

where, $Z_0$ is characteristics impedance equal to 50 $\Omega$ and parasitic capacitance is calculated using equation (15) [1].

$$C_p = \varepsilon_0\varepsilon_r \frac{w_s}{h} \quad (15)$$

The estimated value of fringing capacitance is 7.35 $\mu F$. The transmission line equivalent circuit of the proposed antenna is depicted in Figure 5.

The investigation also carried out to calculate the coupling factor between the two slots, viz. rectangular ring and horizontal slit. The calculated coupling factors is about 0.195 which is sufficiently less [12]. As the coupling factor is low, horizontal slit is independently resonating near 3.5 GHz and does not affect the performance of the antenna. The coupling factor between the two slots is expressed using the equations (16), (17) and (18) [1,12].

$$f_0 = \frac{g_m}{g_s} \quad (16)$$

$$g_m = \frac{k_0}{\pi\eta_0} \left\{ \left( 1 - \frac{s^2}{4} \right) f_0(l) + \frac{s^2}{4} f_2(l) \right\} \quad (17)$$

$$g_s = \frac{k_0}{2\eta_0} \left( 1 - \frac{s^2}{24} \right) \quad (18)$$

In equations (16), (17) and (18), $g_m$ is mutual conductance, $g_s$ is per unit length self- conductance, $S = k_0\ h\ \eta_0$ is 120 $\pi$ and $l = k_0\ q$. The separation between two slots with reference to their center is denoted by $q=12$ mm. In the next section, the detailed results of the proposed antenna are presented and discussed.

4. Results and discussion

The return loss characteristics of all the three antennas is compared and shown in Figure 6. Antenna I resonating at 2.25 GHz realizes the impedance bandwidth of 53.38 % (1.84 – 3.18 GHz), which is useful for lower WLAN/Wi-Fi application. Slit embedded rectangular ring monopole antenna (Antenna II) exhibits dual-band response at 2.15 GHz and 3.46 GHz having a bandwidth of 35.3 % (1.8-2.57 GHz) and 23.9 % (3.16-4.02 GHz), respectively. It is observed that the resonant frequency shifted towards the lower side with some depression in impedance bandwidth. In antenna III, the stub of quarter wavelength at higher resonating frequency acts as monopole and contributes to third resonant frequency. The realized tri-band frequencies are 2.25 GHz (1.87-2.8 GHz), 3.51 GHz (3.2-4 GHz) and 6.04 GHz (5.4-6.5 GHz). The proposed design of monopole antenna exhibits impedance bandwidth of 39.82 %, 22.2 %, and 18.4 % at 2.25 GHz, 3.51 GHz and 6.04 GHz, respectively.

![Fig.4. Equivalent circuit of microstrip line fed RRMPA with slot](image)

![Fig.5. Equivalent circuit of proposed antenna](image)
The variation in second resonance frequency with the change in slit dimensions is depicted in Figure 7. It is observed that by varying the length of slit, middle resonance frequency is shifted towards the higher side, while the change in slit width does not alter resonance frequency. It is also observed that the slit length changes the impedance at higher frequency.

The addition of quarter wavelength stub to partial ground plane yields third resonance at 6.04 GHz, covering higher WLAN (5.725-5.825 GHz) wireless application with 18.4% impedance bandwidth. The effect of variation of stub width is studied and depicted in Figure 8. It is observed that varying the width of the stub, the resonance frequency of third resonance shifted slightly towards the higher frequency side. The surface current distributions at all three resonance frequencies are shown in Figure 9. It is depicted from Figure 9 (a) that the rectangular ring contributes to the lower resonance frequency. The slot of nearly quarter wavelength contributing second resonance frequency and stub placed at ground generates third resonance frequency as current density is higher at slit and stub as shown in Figure 9 (b) and (c), respectively.

The simulated radiation patterns in E- and H- planes for tri-band monopole antenna are shown in Figure 10. The antenna offers peak gain of about 1.7 dBi, 1.35 dBi and 1 dBi at 2.25 GHz, 3.5 GHz, and 6.04 GHz, respectively.

The observed radiation pattern is in broadside direction for all three frequencies. The simulated efficiency and gain are shown in Figure 11. The observed efficiency of proposed antenna is near to 80 to 85 % for lower and middle resonance frequency, respectively and 65% for higher frequency.
The designed and simulated antenna structure is fabricated using FR4 substrates. The fabricated antenna structure is tested using R & S ZVL VNA. The measured results are found to be in good agreement with simulated results. The measured and simulated results are compared and presented in Figure 12. The measured bandwidths are 28% (2.22–2.97 GHz), 5% (3.65–3.87 GHz) and 20% (5.23–6.51 GHz). The performance of the proposed antenna is compared with previously reported antenna designs and compared in Table 2.

5. Conclusion

In this paper, design of tri-band microstrip antenna for wireless communication applications is presented. The antenna resonates at 2.25 GHz, 3.5 GHz and 6.04 GHz with simulated and measured impedance bandwidth of 39.82 %, 22.2 %, 18.4 % and 28 %, 5 % and 20 % respectively. The designed antenna radiates in broadside direction for all three bands with maximum gain of 1.7 dBi. The antenna fulfills broadband response for tri-band applications. The antenna is tested and measured results are found in good agreement. The performance of proposed antenna is compared with previously reported results and presented. The antenna can be useful for wireless applications.

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Table 2: Performance comparison with previously reported work

<table>
<thead>
<tr>
<th>Ref.</th>
<th>Size (mm^2)</th>
<th>εr</th>
<th>Thickness h (mm)</th>
<th>Band-I Fractional Bandwidth (%)</th>
<th>Band-II Fractional Bandwidth (%)</th>
<th>Band-III Fractional Bandwidth (%)</th>
</tr>
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<tbody>
<tr>
<td>[4]</td>
<td>34 × 18</td>
<td>4.4</td>
<td>1.6</td>
<td>8.73</td>
<td>8.74</td>
<td>24.11</td>
</tr>
<tr>
<td>[6]</td>
<td>40 × 29</td>
<td>4.4</td>
<td>1</td>
<td>20.35</td>
<td>9.3</td>
<td>14</td>
</tr>
<tr>
<td>[7]</td>
<td>12 × 37.3</td>
<td>3.2</td>
<td>0.44</td>
<td>3.21</td>
<td>28.1</td>
<td>36</td>
</tr>
<tr>
<td>[9]</td>
<td>29 × 25</td>
<td>4.4</td>
<td>1.6</td>
<td>4.9</td>
<td>9.1</td>
<td>31.65</td>
</tr>
<tr>
<td>Proposed Work</td>
<td>60 × 40</td>
<td>4.3</td>
<td>1.6</td>
<td>28.9</td>
<td>5.85</td>
<td>21.8</td>
</tr>
</tbody>
</table>

References

[13] Altair’s Hyperwork CAD FEKO, Altair Engineering Inc. USA.

Biography of the authors

Mandar P. Joshi received B.E. in Electronics and Telecommunication Engineering from North Maharashtra University, Jalgaon and M.E. in Electronics from Dr. Babasaheb Ambedkar Marathwada University, Aurangabad (MS) India in 2003 and 2012 respectively. Presently, he is working as Assistant Professor in E & TC at CES’s R. H. Sapat College of Engineering, Management Studies and Research, Nashik (MS), India. His research interest includes microstrip patch antennas for Wi-Fi, WLAN, Wi-MAX, BAN, broadband microstrip antennas, wearable microstrip antenna.

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Dr. Joshi has published three books titled: (1) Mechatronics (2006, Published by Prentice Hall of India; New Delhi, India) (2) Electronic Measurement and Instrumentation Systems (2001, Khanna Book Publishing Co; New Delhi, India), (3) Metamaterial Loaded Antennas for Wireless Communication (2014, LAP LAMBERT Academic Publishing, Germany). His book chapters on metamaterial and wearable microstrip patch antennas have been respectively published in “Encyclopedia of Information Science and Technology” and “Handbook of Research on Progressive Trends in Wireless Communications and Networking” by IGI Global, USA. He has 80 technical research papers to his credit.

He is a recipient of IEEE Rajneesh Arora Best Research Paper Award in IEEE IAW 2014, Chandigarh, India. First Prize for Research Paper at Institution of Engineers (India), Pune Local.
Centre, Annual Technical Paper Meet. His research interests include Metamaterial and microstrip patch antennas, Wearable antennas (for Wi-Fi, Wi-Max, WLAN, BAN, MBAN and public safety band applications), Mechatronics, and Instrumentation systems.

**Prof. Shyam Sundar Pattnaik** is presently the Director of National Institute of Technical Teachers Training and Research (NITTTR), Chandigarh, India. The institute is an autonomous institute of Ministry of Human Resource and Development, Govt. of India established in the year 1967. Before joining to the post of Director, Prof. Pattnaik served as Vice-Chancellor of Biju Patnaik University of Technology, a Technical University of Govt. of Odisha. Prof. Pattnaik completed his Post. Doctoral Research from University of Utah, USA and Ph.D. in Engineering (Electronics and Telecommunication Engineering) from Sambalpur University, Odisha. Under the able supervision and guidance of Prof. S S Pattnaik, 21 (Twenty one) students have completed their PhDs and five more are pursuing their PhD work and he has 52 ME thesis to his credit. He has published more than 250 papers in journals and conferences of repute. He has two international books and four international book chapters to his credit. A recipient of BOYSCAST FELLOWSHIP from DST, Govt. of India, Prof. Pattnaik also is awarded with SERC Visiting Fellowship, INSA Visiting Fellowship, UGC Visiting Fellowship, Distinguished Faculty Award, National Educational Excellence Award, National scholarship, Best paper Award etc. He is also listed in WHO’S WHO in the WORLD. He is a Life Fellow of IETE, Life Member of ISTE and Senior Member of IEEE, USA.